

21867
#8
5-21-03
ca



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Matthew J. Adiletta, et al. Art Unit : Unknown
Serial No. : 09/387,109 Examiner : Unknown
Filed : August 31, 1999
Title : SDRAM CONTROLLER FOR PARALLEL PROCESSOR ARCHITECTURE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RECEIVED

MAY 19 2003

INFORMATION DISCLOSURE STATEMENT

Technology Center 2100

Copies of the references listed on the attached form PTO-1449 are enclosed. A copy of a communication from a foreign patent office in a counterpart application is also enclosed.

This statement is being filed within three months of the filing date of the application or before the receipt of a first Office action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: May 12, 2003

Kenneth F. Kozik
Kenneth F. Kozik
Reg. No. 36,572

Fish & Richardson P.C.
225 Franklin Street
Boston, Massachusetts 02110-2804
Telephone: (617) 542-5070
Facsimile: (617) 542-8906

20644873.doc

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Date of Deposit 5/12/03
Alissa Passacantilli
Signature

Alissa Passacantilli
Typed or Printed Name of Person Signing Certificate